

**AHSANULLAH UNIVERSITY OF SCIENCE AND TECHNOLOGY**

**Department of Computer Science & Engineering**

**Course No : CSE2106**

**Course Title : Digital Logic Design Sessional**

**Experiment No : 07**

**Experiment Name : Design of Adder and Subtractor**

**Date of Submission : 28.08.2021**

**Submitted to: Ms. Qamrun Nahar Eity & Samin Shahriar**

**Submitted By-**

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**Section : B1**